



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

lication of

Keiichi KUSUMOTO

Serial No.: 10/084,978

Group Art Unit: 2811

Filed: March 01, 2002

Examiner:

For:

: ; INTEGRATED CIRCUIT DEVICE AND METHOD FOR FORMING THE SAME

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, DC 20231

Dear Sir

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Supplemental Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION O 1 P APPLICATION O				ATTY, DOCKET NO. 60188-155		SERIAL NO. 10/084,978			
				APPLICANT Keiichi KUSUMOTO					
				FILING DATE March 01, 20	GROUP 2811				
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EXAMINER'S			COUNTRY		CLASS	SUBCLASS	Translation		
INITIALS	PATENT NO.	DATE					Yes	,	No
	8-321551	12/3/1996 Japan (w/ English Abstract)							
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

EXAMINER

DATE CONSIDERED